

FIG. 1

2/10

FIG. 2

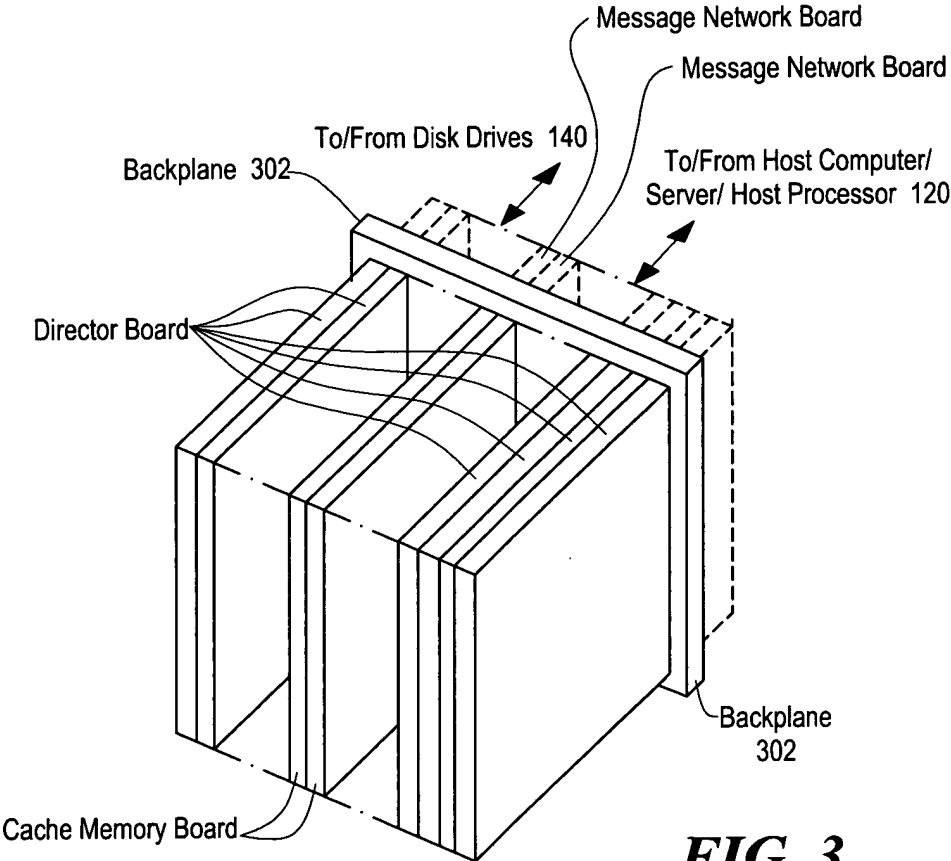
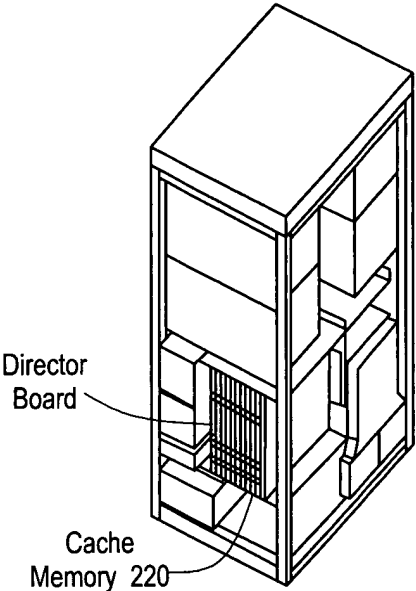


FIG. 3

3/10

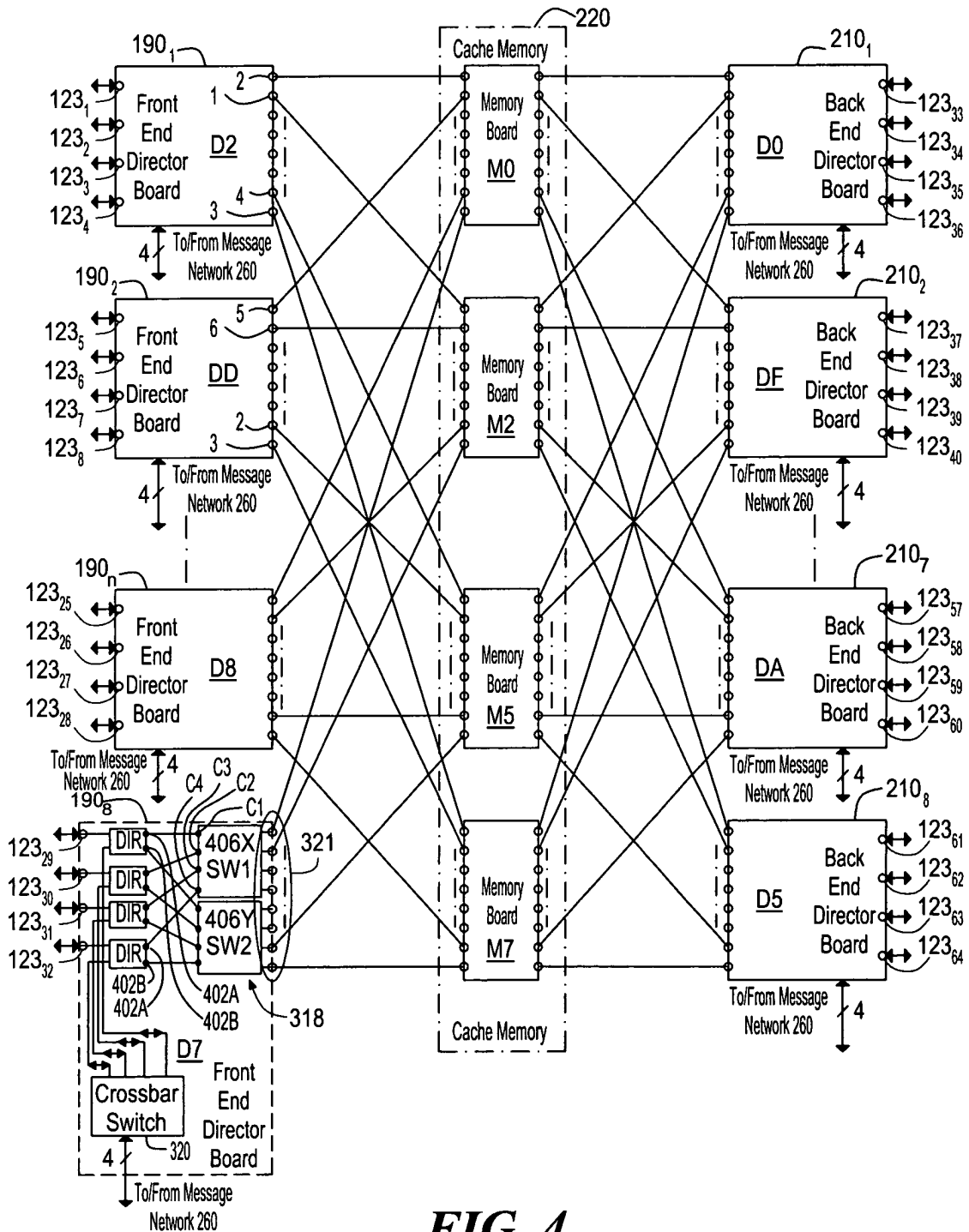


FIG. 4

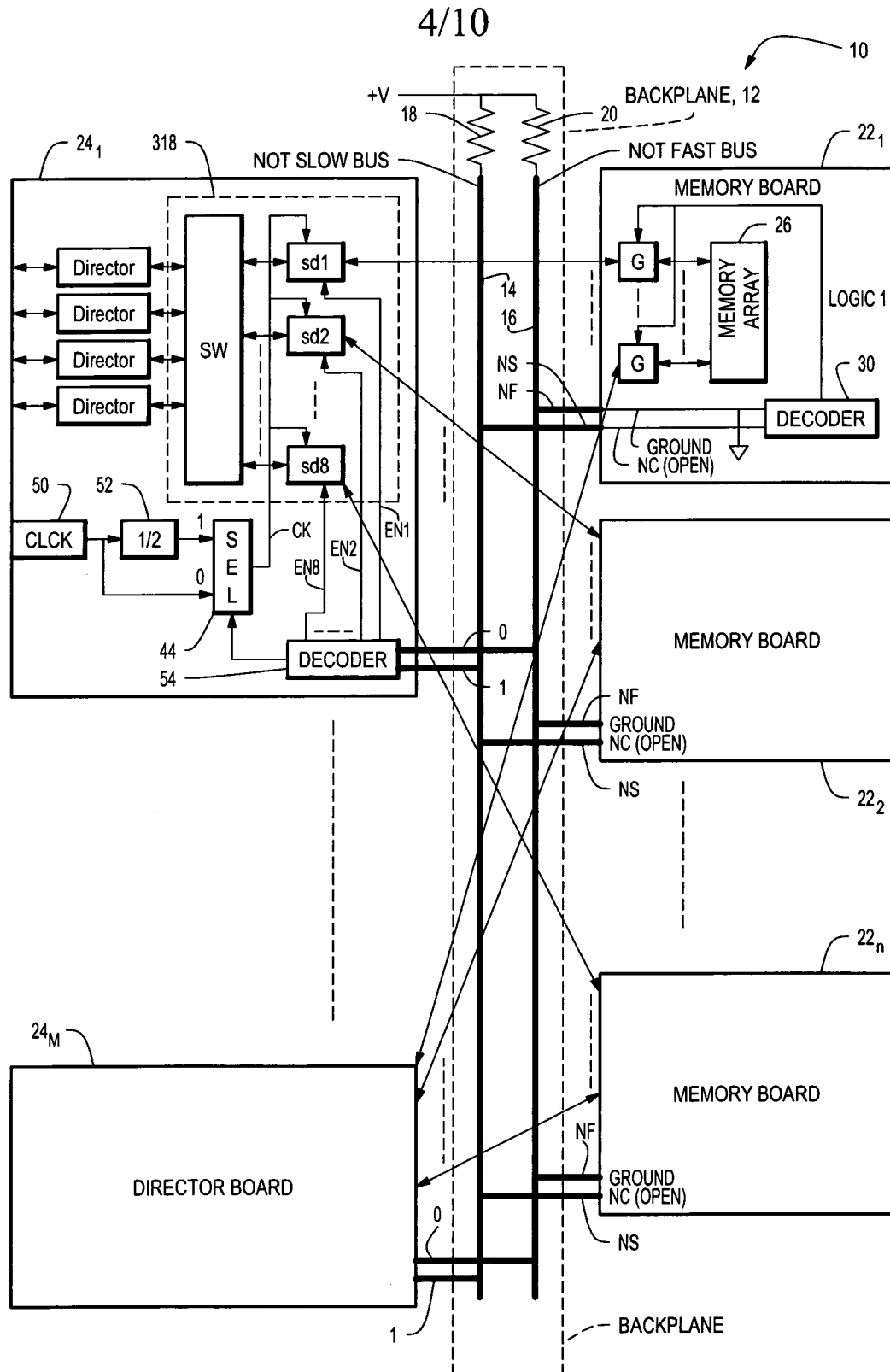


FIG. 5

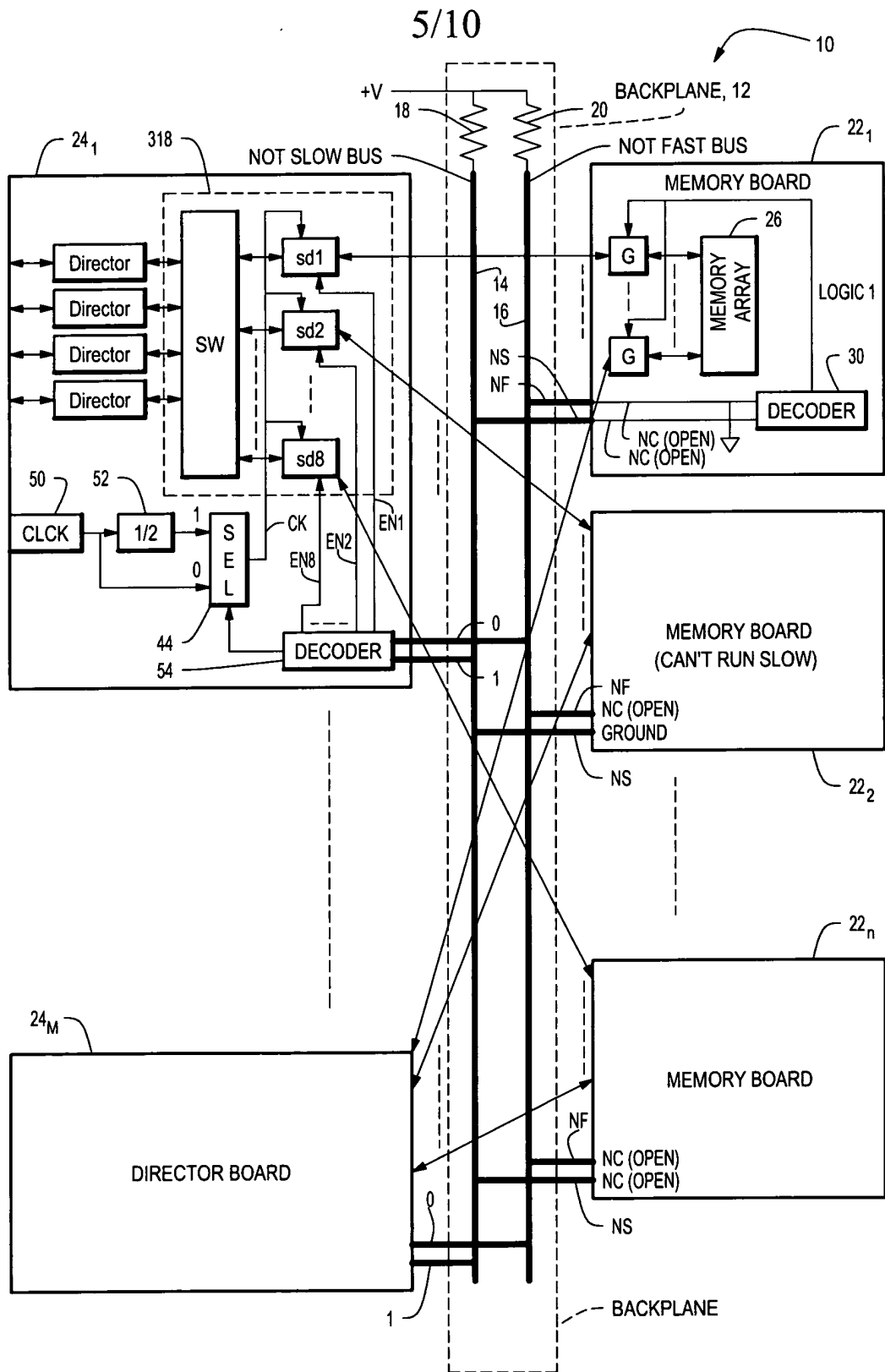


FIG. 6

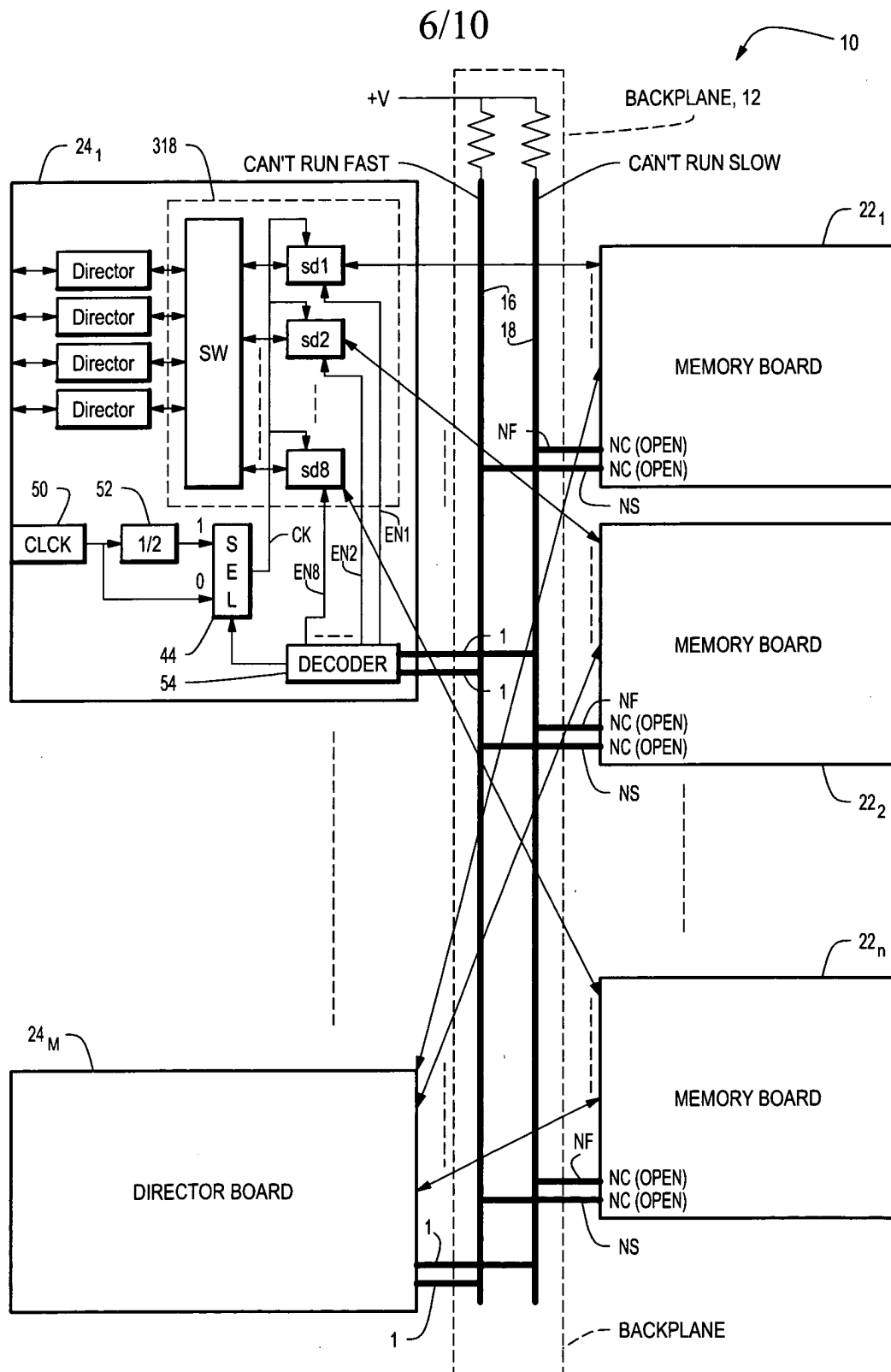


FIG. 7

SYSTEM HAVING BACKPLANE PERFORMANCE CAPABILITY SELECTION LOGIC

John K. Walton et al.

10/606,872

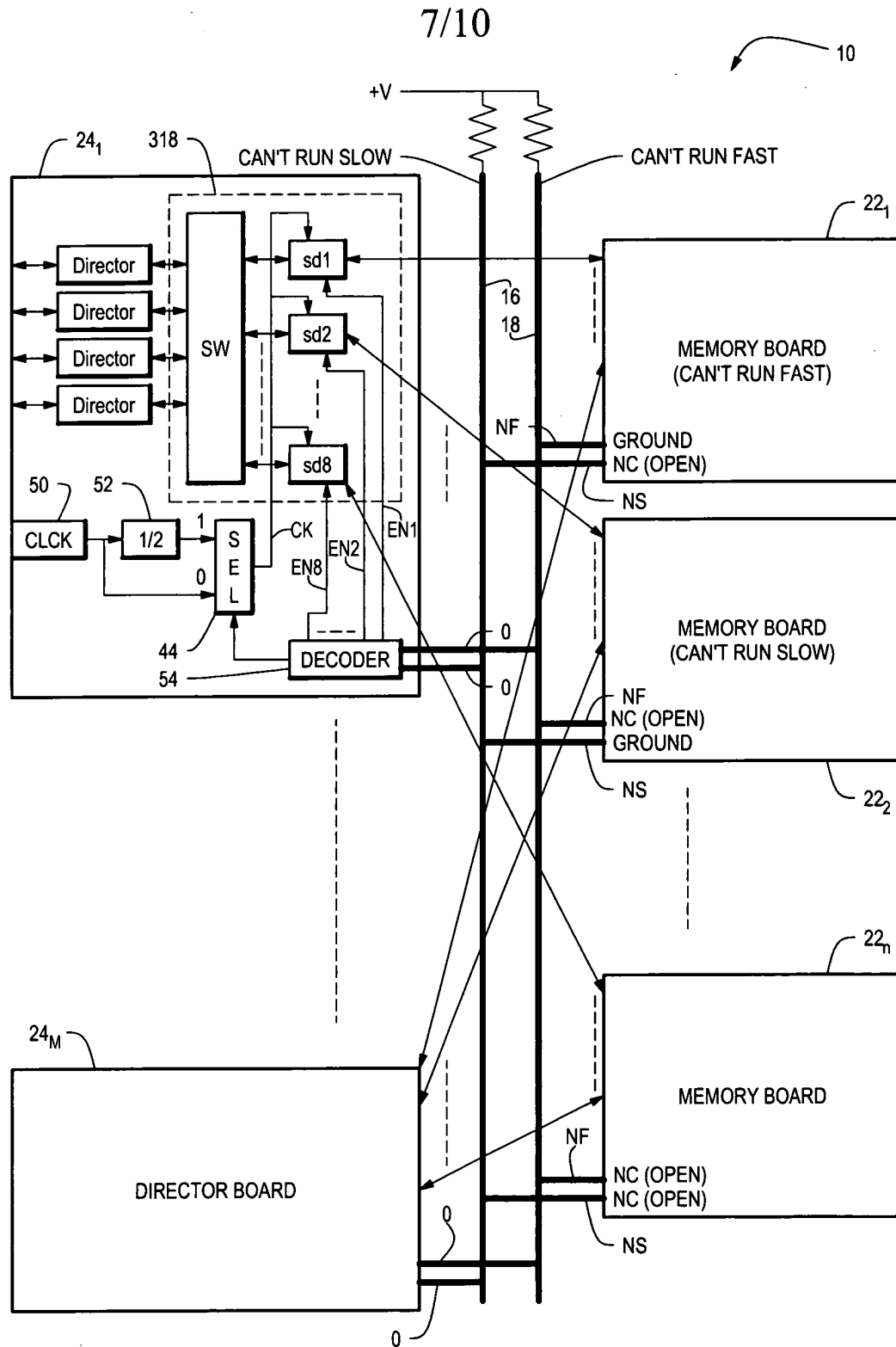


FIG. 8

SYSTEM HAVING BACKPLANE PERFORMANCE CAPABILITY SELECTION LOGIC

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10/606,872

8/10

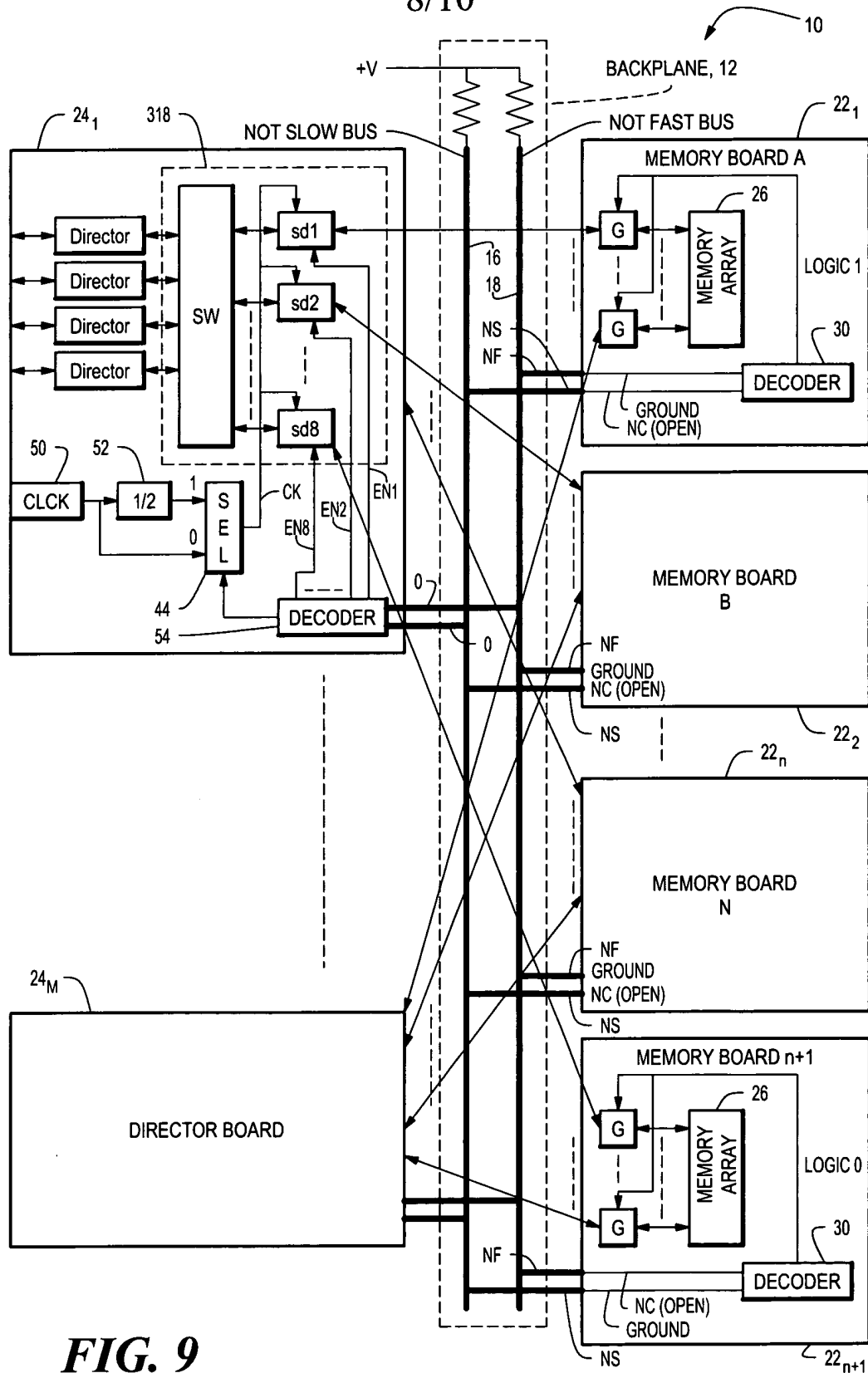


FIG. 9

9/10

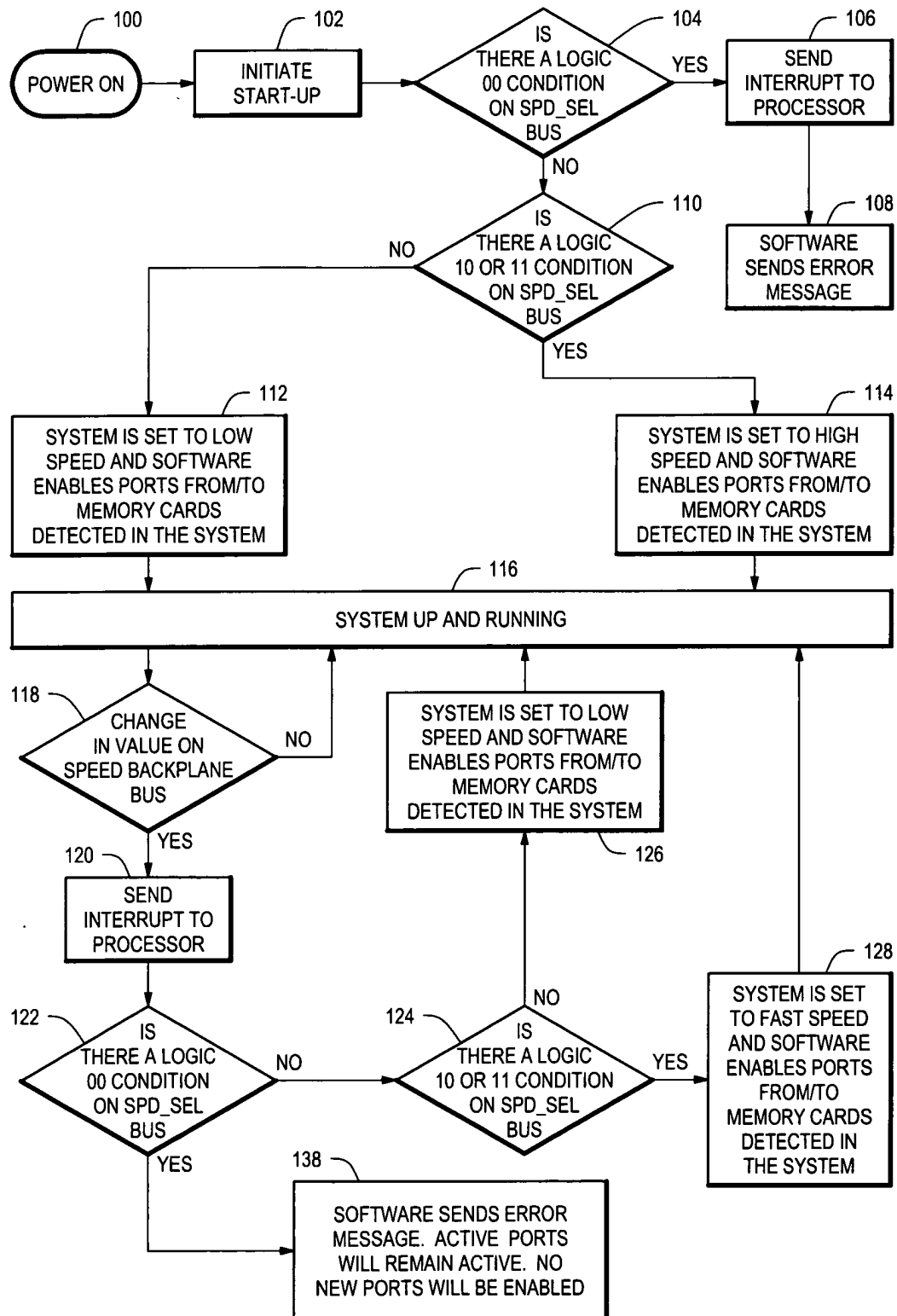


FIG. 10

10/10

